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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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•	Application No.	Applicant(s)			
	10/786,864	MIYANARI ET AL.			
Office Action Summary	Examiner	Art Unit			
	Usman Khan	2622			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).					
Status					
 Responsive to communication(s) filed on <u>RCE filed 12/06/2007</u>. This action is FINAL. 2b) ☐ This action is non-final. Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i>, 1935 C.D. 11, 453 O.G. 213. 					
Disposition of Claims					
4) Claim(s) 1-6,8,9 and 11 is/are pending in the at 4a) Of the above claim(s) is/are withdraw 5) Claim(s) is/are allowed. 6) Claim(s) 1-6,8,9 and 11 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or are subject to restriction and/or are subject to by the Examine 10) The specification is objected to by the Examine Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction 11) The oath or declaration is objected to by the Examine Replacement drawing sheet(s) including the correction 11) The oath or declaration is objected to by the Examine 11) The oath or declaration is objected to by the Examine 11) The oath or declaration is objected to by the Examine 11) The oath or declaration is objected to by the Examine 11) The oath or declaration is objected to by the Examine 11 The oath or declaration is objected	vn from consideration. r election requirement. r. e: a)⊠ accepted or b)□ objected drawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
12) ⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) ⊠ All b) ☐ Some * c) ☐ None of: 1. ☑ Certified copies of the priority documents have been received. 2. ☐ Certified copies of the priority documents have been received in Application No 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.					
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	ite			

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A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 12/06/2007 has been entered.

Response to Arguments

Applicant's arguments filed on 1/06/2007 with respect to claims 1-6, 8, 9, and 11 have been considered but are moot in view of the new ground(s) of rejection.

DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 1- 3, 5 - 6, 9, and 11 rejected under 35 U.S.C. 103(a) as being unpatentable over Kohashi et al. (US patent No 6,642,960) in further view of SHIOMI (JP2001016509A).

Regarding claim 1, Kohashi et al. teaches an image sensing apparatus (abstract and column 2 lines 5 et seq.) comprising: a plurality of pixels arrayed in a horizontal and 10/786,864

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a vertical direction (column 2 lines 14 – 34, image pickup device composed of two-dimensionally arrayed pixels); a first calculating portion which creates correction data by performing computation using signals which are acquired by image sensing in an unexposed state (figure 4A item 21-1 and column 13 lines 4 *et seq.*) and smaller in number than said plurality of pixels (column 12 lines 13 *et seq.* and column 13 lines 51 *et seq.*, region surrounding a fault pixel); and a second calculating portion which corrects image data of said plurality of pixels, acquired by image sensing in an exposed state, by using the correction data (figure 5 items 31-1 *et seq.* and column 13 lines 62 *et seq.*; and correcting each pixel is taught in column 2 lines 52 – 62, column 6 lines 50 – 62 column 13 lines 4 – 40; also figures 24 – 27 and column 21 lines 38 *et seq.* Kohashi et al. teaches that the faulty pixel group can be composed of more then one pixel for correcting i.e. plurality of pixels).

However, Kohashi et al. fails to teach that the correction data is one-dimensional data in the horizontal direction and the second correcting portion is that of said plurality of pixels for image data of each of pixels arrayed in the horizontal direction. SHIOMI, on the other hand teaches that the correction data is one-dimensional data in the horizontal direction and the second correcting portion is that of said plurality of pixels for image data of each of pixels arrayed in the horizontal direction.

More specifically, SHIOMI teaches that the correction data is one-dimensional data in the horizontal direction and the second correcting portion is that of said plurality of pixels for image data of each of pixels arrayed in the horizontal direction (paragraphs 0029 - 0040).

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Therefore, one of ordinary skill in the art at the time the invention was made would have been motivated to incorporate the teachings of SHIOMI with the teachings of Kohashi et al. because in paragraph 0043 SHIOMI teaches that the use of the invention reduces noise.

Regarding **claim 2**, as mentioned above in the discussion of claim 1, Kohashi et al. in further view of SHIOMI teaches all of the limitations of the parent claim. Additionally, Kohashi et al. teaches that said first calculating portion changes the number of signals to be used-for creation of correction data in accordance with a sensitivity condition set at the time of image sensing (figure 9A – 9K; also column 14 lines 59 et seq. the pattern changes on an edge condition resulting in a varying signal used for correction).

However, Kohashi et al. fails to teach that the correction data is one-dimensional data in the horizontal direction. SHIOMI, on the other hand teaches that the correction data is one-dimensional data in the horizontal direction.

More specifically, SHIOMI teaches that the correction data is one-dimensional data in the horizontal direction (paragraphs 0029 - 0040).

Therefore, one of ordinary skill in the art at the time the invention was made would have been motivated to incorporate the teachings of SHIOMI with the teachings of Kohashi et al. because in paragraph 0043 SHIOMI teaches that the use of the invention reduces noise.

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Regarding claim 3, as mentioned above in the discussion of claim 1, Kohashi et al. in further view of SHIOMI teaches all of the limitations of the parent claim. Additionally, Kohashi et al. teaches that the said plurality of pixels are arrayed in the horizontal direction and the vertical direction (column 2 lines 14 - 34, image pickup device composed of two-dimensionally arrayed pixels; also figures 1, 2, 7, 9, 10, 12, 16, 18-22, and 24-47), and said first calculating portion creates the correction data by vertically mixing signals (column 13 line 62 - column 14 line 12; vertical direction pixel interpolating) from pixels which are smaller in number than said plurality of pixels and arrayed in the horizontal direction and the vertical direction (column 13 line 62 - column 14 line 12 region surrounding a fault pixel; and column 12 lines 13 et seq. and column 13 lines 51 et seq., region surrounding a fault pixel).

However, Kohashi et al. fails to teach that the correction data is one-dimensional data in the horizontal direction. SHIOMI, on the other hand teaches that the correction data is one-dimensional data in the horizontal direction.

More specifically, SHIOMI teaches that the correction data is one-dimensional data in the horizontal direction (paragraphs 0029 - 0040).

Therefore, one of ordinary skill in the art at the time the invention was made would have been motivated to incorporate the teachings of SHIOMI with the teachings of Kohashi et al. because in paragraph 0043 SHIOMI teaches that the use of the invention reduces noise.

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Regarding **claim 5**, as mentioned above in the discussion of claim 1, Kohashi et al. teaches all of the limitations of the parent claim. Additionally, it is inherent that the first calculation portion will operate in accordance with when the image sensing apparatus is powered on via a power switch.

Regarding **claim 6**, Kohashi et al. teaches a control method for an image sensing apparatus (abstract and column 2 lines 5 *et seq.*) having a plurality of pixels arrayed in a horizontal and vertical direction (column 2 lines 14 – 34, image pickup device composed of two-dimensionally arrayed pixels); comprising: a first calculating step which creates correction data by performing computation using signals which are acquired by image sensing in an unexposed state (figure 4A item 21-1 and column 13 lines 4 *et seq.*) and smaller in number than said plurality of pixels (column 12 lines 13 *et seq.* and column 13 lines 51 *et seq.*, region surrounding a fault pixel); and a second calculating step which corrects image data of said plurality of pixels, acquired by image sensing in an exposed state, by using the correction data (figure 5 items 31-1 *et seq.* and column 13 lines 62 *et seq.*).

However, Kohashi et al. fails to teach that the correction data is one-dimensional data in the horizontal direction and the second correcting portion is that of said plurality of pixels for image data of each of pixels arrayed in the horizontal direction. SHIOMI, on the other hand teaches that the correction data is one-dimensional data in the horizontal direction and the second correcting portion is that of said plurality of pixels for image data of each of pixels arrayed in the horizontal direction.

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More specifically, SHIOMI teaches that the correction data is one-dimensional data in the horizontal direction and the second correcting portion is that of said plurality of pixels for image data of each of pixels arrayed in the horizontal direction (paragraphs 0029 - 0040).

Therefore, one of ordinary skill in the art at the time the invention was made would have been motivated to incorporate the teachings of SHIOMI with the teachings of Kohashi et al. because in paragraph 0043 SHIOMI teaches that the use of the invention reduces noise.

Regarding **claim 9**, as mentioned above in the discussion of claim 1, Kohashi et al. teaches all of the limitations of the parent claim. Additionally, Kohashi et al. teaches that only signals of a smaller number than said plurality of pixels to be corrected are acquired by image sensing in an unexposed state to create the correction data (column 13 line 62 – column 14 line 12 region surrounding a fault pixel; and column 12 lines 13 et seq. and column 13 lines 51 et seq., region surrounding a fault pixel).

However, Kohashi et al. in further view of Hamasaki fa ils to teach that the correction data is one-dimensional data in the horizontal direction. SHIOMI, on the other hand teaches that the correction data is one-dimensional data in the horizontal direction.

More specifically, SHIOMI teaches that the correction data is one-dimensional data in the horizontal direction (paragraphs 0029 - 0040).

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Therefore, one of ordinary skill in the art at the time the invention was made would have been motivated to incorporate the teachings of SHIOMI with the teachings of Kohashi et al. in further view of Hamasaki because in paragraph 0043 SHIOMI teaches that the use of the invention reduces noise.

Regarding **claim 11**, as mentioned above in the discussion of claim 1, Kohashi et al. teaches all of the limitations of the parent claim. Additionally, Kohashi et al. teaches that the second calculating portion uses the correction data to correct for noise in the image data (column 11 line 66 – column 12 line 12, noise canceling).

However, Kohashi et al. in further view of Hamasaki fa ils to teach that the correction data is one-dimensional data in the horizontal direction. SHIOMI, on the other hand teaches that the correction data is one-dimensional data in the horizontal direction.

More specifically, SHIOMI teaches that the correction data is one-dimensional data in the horizontal direction (paragraphs 0029 - 0040).

Therefore, one of ordinary skill in the art at the time the invention was made would have been motivated to incorporate the teachings of SHIOMI with the teachings of Kohashi et al. in further view of Hamasaki because in paragraph 0043 SHIOMI teaches that the use of the invention reduces noise.

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Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kohashi et al. (US patent No 6,642,960) in further view of Hamasaki (US patent No 5,335,008) in further view of SHIOMI (JP2001016509A).

Regarding **claim 4**, as mentioned above in the discussion of claim 3, Kohashi et al. teaches all of the limitations of the parent claim. Additionally, Kohashi et al. teaches that said first calculating portion creates the correction data by vertically mixing signals from pixels (column 13 line 62 – column 14 line 12; vertical direction pixel interpolating), which are smaller in number than said plurality of pixels and arrayed in the horizontal direction and the vertical direction, through the corresponding amplifiers (column 13 line 62 – column 14 line 12 region surrounding a fault pixel; and column 12 lines 13 *et seq.* and column 13 lines 51 *et seq.*, region surrounding a fault pixel).

However, Kohashi et al. fails to disclose an amplifier for each array of pixels arrayed and plurality of pixels and arrayed in the horizontal direction and the vertical direction, through the corresponding amplifiers. Hamasaki, on the other hand discloses an amplifier for each array of pixels arrayed and plurality of pixels and arrayed in the horizontal direction and the vertical direction, through the corresponding amplifiers.

More specifically, Hamasaki discloses an amplifier for each array of pixels arrayed and plurality of pixels and arrayed in the horizontal direction and the vertical direction, through the corresponding amplifiers (column 2 lines 30 – 41).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the teachings of Hamasaki with the teachings of Kohashi et al. so that the fluctuation of a threshold voltage of a load MOS

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transistor connected to the vertical signal line can be reduced so that an aperture ratio can be increased as the vertical signal line is reduced in thickness as taught in column 2 lines 24 – 29 of Hamasaki.

However, Kohashi et al. in further view of Hamasaki fa ils to teach that the correction data is one-dimensional data in the horizontal direction. SHIOMI, on the other hand teaches that the correction data is one-dimensional data in the horizontal direction.

More specifically, SHIOMI teaches that the correction data is one-dimensional data in the horizontal direction (paragraphs 0029 - 0040).

Therefore, one of ordinary skill in the art at the time the invention was made would have been motivated to incorporate the teachings of SHIOMI with the teachings of Kohashi et al. in further view of Hamasaki because in paragraph 0043 SHIOMI teaches that the use of the invention reduces noise.

Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kohashi et al. (US patent No 6,642,960) in further view of Examiners Official Notice in further view of SHIOMI (JP2001016509A).

Regarding **8**, Kohashi et al. teaches a computer implements a control method for an image sensing apparatus (abstract and column 2 lines 5 *et seq.*) having a plurality of pixels arranged in a horizontal and a vertical direction (column 2 lines 14 – 34, image pickup device composed of two-dimensionally arrayed pixels), the method comprising:

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a first calculating step which creates correction data by performing computation using signals which are acquired by image sensing in an unexposed state (figure 4A item 21-1 and column 13 lines 4 et seq.) and smaller in number than said plurality of pixels (column 12 lines 13 et seq. and column 13 lines 51 et seq., region surrounding a fault pixel); and

a second calculating step which corrects image data of each of said plurality of pixels, acquired by image sensing in an exposed state, by using the correction data (figure 5 items 31-1 et seq. and column 13 lines 62 et seq.; and correcting each pixel is taught in column 2 lines 52 – 62, column 6 lines 50 – 62 column 13 lines 4 – 40; also figures 24 – 27 and column 21 lines 38 et seq. Kohashi et al. teaches that the faulty pixel group can be composed of more then one pixel for correcting i.e. plurality of pixels).

However, Kohashi et al. fails to teach a computer readable medium storing program code that is executed by the computer.

The examiner takes Official Notice that it is old and well known in the art to have a computer readable medium storing program code that is executed by a computer.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use a computer readable medium storing program code that is executed by a computer since the computer readable medium is easily upgradeable.

However, Kohashi et al. in further view of Examiners Official Notice fails to teach that the correction data is one-dimensional data in the horizontal direction and the

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second correcting portion is that of said plurality of pixels for image data of each of pixels arrayed in the horizontal direction. SHIOMI, on the other hand teaches that the correction data is one-dimensional data in the horizontal direction and the second correcting portion is that of said plurality of pixels for image data of each of pixels arrayed in the horizontal direction.

More specifically, SHIOMI teaches that the correction data is one-dimensional data in the horizontal direction and the second correcting portion is that of said plurality of pixels for image data of each of pixels arrayed in the horizontal direction (paragraphs 0029 - 0040).

Therefore, one of ordinary skill in the art at the time the invention was made would have been motivated to incorporate the teachings of SHIOMI with the teachings of Kohashi et al. in further view of Examiners Official Notice because in paragraph 0043 SHIOMI teaches that the use of the invention reduces noise.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Usman Khan whose telephone number is (571) 270-1131. The examiner can normally be reached on Mon-Thru 6:45-4:15; Fri 6:45-3:15 or Alt. Fri off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Ometz can be reached on (571) 272-7593. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Úsman Khan 12/28/2007

Patent Examiner Art Unit 2622

PRIMARY EXAMINER